

A1

(12) INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(19) World Intellectual Property Organization
International Bureau



(43) International Publication Date
25 January 2001 (25.01.2001)

PCT

(10) International Publication Number
WO 01/06821 A1

(51) International Patent Classification⁷: H05K 7/20 (81) Designated States (national): AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BY, BZ, CA, CH, CN, CR, CU, CZ, DE, DK, DM, DZ, EE, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, MZ, NO, NZ, PL, PT, RO, RU, SD, SE, SG, SI, SK, SL, TJ, TM, TR, TT, TZ, UA, UG, UZ, VN, YU, ZA, ZW.

(21) International Application Number: PCT/US00/18081 (84) Designated States (regional): ARIPO patent (GH, GM, KE, LS, MW, MZ, SD, SL, SZ, TZ, UG, ZW), Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, CH, CY, DE, DK, ES, FI, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, GW, ML, MR, NE, SN, TD, TG).

(22) International Filing Date: 28 June 2000 (28.06.2000)

(25) Filing Language: English

(26) Publication Language: English

(30) Priority Data: 09/353,428 15 July 1999 (15.07.1999) US

(71) Applicant: INCEP TECHNOLOGIES, INC. [US/US]; 10650 Treena Street, Suite 308, San Diego, CA 92131 (US).

(72) Inventors: DIBENE II, Joseph, Ted; 19 Shasta Court, Oceanside, CA 92057 (US). HARTKE, David; 340 C.R. 245, Durango, CO 81301 (US).

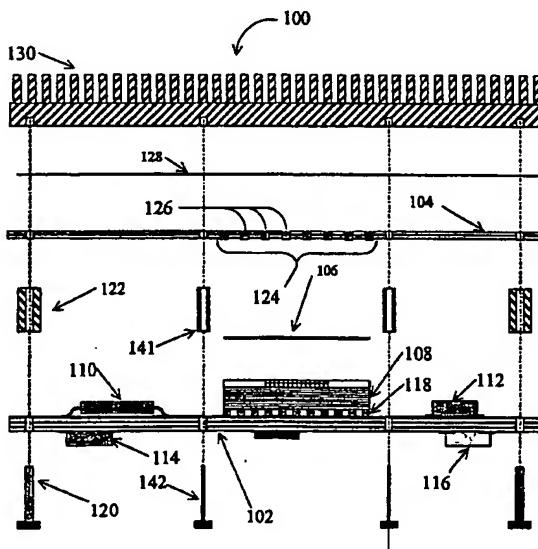
(74) Agent: ORLER, Anthony, J.; Gates & Cooper, 6701 Center Drive West, Suite 1050, Los Angeles, CA 90045 (US).

Published:

— With international search report.
— With amended claims.

For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.

(54) Title: ENCAPSULATED PACKAGING IN BETWEEN 2 PCBS



WO 01/06821 A1

(57) Abstract: An encapsulated circuit assembly and a method for making an encapsulated circuit assembly are disclosed. The assembly comprises a first printed circuit board, a second printed circuit board, and a heat transfer device. The second printed circuit board comprises a heatsink, and the heat transfer device couples between a device mounted on the first printed circuit board and the second printed circuit board for transferring heat from the device to the heatsink of the second printed circuit board.

ENCAPSULATED PACKAGING IN BETWEEN 2 PCBs

BACKGROUND OF THE INVENTION

1. Field of the Invention.

This invention relates in general to a methodology to improve thermal and mechanical issues created by increased interconnect density, increased power levels by 5 electronic circuits and increased levels of integrated electronic packaging. The present invention addresses these issues by encapsulating the circuitry within a circuit board structure which improves thermal, mechanical and integrated circuit device management over existing technologies known in the art today.

10 2. Description of Related Art.

As circuitry in electronics becomes more and more complex, packaging of the circuitry has become more difficult. The common method for packaging integrated circuits and other electronic components is to mount them on Printed Circuit Boards (PCBs).

15 Recently, the application of new organic laminates in the construction of Multi-Chip-Modules (MCMs) has brought about significant improvements in the packaging cost and density of electronic circuits. Throughout this patent reference will be made to PCBs which shall be meant to include technologies associated with MCMs as well.

Computer chip clocking speeds have also increased. This increase in speed has 20 made it difficult to couple chips together in such a way that the chip speeds are completely useable. Further, heat generated by integrated circuits has increased because of the increased number of signals travelling through the integrated circuits. In addition, as die size increases interconnect delays on the die are beginning to limit the circuit speeds within the die. Typically, the limitations of a system are contributed to, in part, by 25 the packaging of the system itself. These effects are forcing greater attention to methods of efficiently coupling high-speed circuits.

Packaging the integrated circuits onto PCBs has become increasingly more difficult because of the signal density within integrated circuits and the requirements of heat dissipation. Typical interconnections on a PCB are made using traces that are 30 etched or pattern plated onto a layer of the PCB. To create shorter interconnections,

Surface Mount Technology (SMT) chips, Very Large Scale Integration (VLSI) circuits, flip chip bonding, Application Specific Integrated Circuits (ASICs), Ball Grid Arrays (BGAs), and the like, have been used to shorten the transit time and interconnection lengths between chips on a PCB. However, this technology has also not completely 5 overcome the needs for higher signal speeds both intra-PCB and inter-PCB, because of thermal considerations, EMI concerns, and other packaging problems.

In any given system, PCB area (also known as PCB "real estate") is at a premium. With smaller packaging envelopes becoming the norm in electronics, e.g., laptop computers, spacecraft, cellular telephones, etc., large PCBs are not available for use to 10 mount SMT chips, BGAs, flip chips or other devices. Newer methods are emerging to decrease the size of PCBs such as Build-Up-Multilayer technology, improved organic laminate materials with reduced thicknesses and dielectric constants and laser beam photo imaging. These technologies produce greater pressure to maintain the functionality of the PCB assembly in thermal, EMI and power application to the 15 semiconductor devices. It can be seen, then, that there is a need in the art for a method for decreasing the size of PCBs while maintaining the functionality of PCBs. Further, there is a need for reducing the size of PCBs while using present-day manufacturing techniques to maintain low cost packaging.

20

SUMMARY OF THE INVENTION

To overcome the limitations in the prior art described above, and to overcome other limitations that will become apparent upon reading and understanding the present specification, the present invention discloses an encapsulated circuit assembly which utilizes printed circuit boards as a foundation for the assembly structure. The assembly 25 comprises a first printed circuit board, a second printed circuit board, and a heat transfer device. The second printed circuit board comprises a thermal heatsink or secondary heat transfer mechanism such as a heat pipe and heat transfer devices imbedded within the second printed circuit board which thermally couples devices mounted on the first printed circuit board and the thermal heat sink of the second printed circuit board.

30

An object of the present invention is to provide more efficient usage of printed circuit board real estate. Another object of the present invention is to increase the

density of electronics on printed circuit boards. Another object of the present invention is to provide heat transfer from devices on printed circuit boards.

These and various other advantages and features of novelty that characterize the invention are pointed out with particularity in the claims annexed hereto and form a part hereof. However, for a better understanding of the invention, its advantages, and the objects obtained by its use, reference should be made to the drawings which form a further part hereof, and to the accompanying detailed description, in which there is illustrated and described specific examples of a method, apparatus, and article of manufacture in accordance with the invention.

10

BRIEF DESCRIPTION OF THE DRAWINGS

Referring now to the drawings in which like reference numbers represent corresponding parts throughout:

FIGS. 1A-1C illustrate the construction of a printed circuit board assembly using the present invention;

FIGS. 2A-2B illustrate the construction of a printed circuit board assembly using the present invention for multiple heat generating integrated circuit devices;

FIG. 3A-3B illustrate spacers which is used in conjunction with the present invention;

FIGS. 4A-4C illustrate the construction of a printed circuit board using the present invention wherein the thermal heat sink is located outboard the active circuit area;

FIGS. 5A and 5B illustrate the thermal considerations of a printed circuit board embodying the present invention; and

FIG. 6 illustrates a flow chart describing the steps used in practicing the present invention.

DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENT

In the following description of the preferred embodiment, reference is made to the accompanying drawings which form a part hereof, and in which is shown by way of illustration a specific embodiment in which the invention may be practiced. It is to be

understood that other embodiments may be utilized and structural changes may be made without departing from the scope of the present invention.

Overview

5 The present invention discloses an encapsulated circuit assembly and a method for making such an assembly. The assembly comprises a first printed circuit board, a second printed circuit board, and heat transfer devices. The second printed circuit board comprises a heatsink or secondary heat transfer mechanism such as heat pipes and heat transfer devices imbedded within the second printed circuit board which thermally
10 couples devices mounted on the first printed circuit board and the thermal heat sink of the second printed circuit board.

The present invention provides a method and apparatus for mounting integrated circuit devices onto PCBs that removes the heat from those devices that generate large amounts of heat. The present invention allows for air cooling, heat pipe cooling, or
15 other methods of cooling devices, as well as a compact packaging design to allow for heat generating devices to be packaged into small volumes. Furthermore, the present invention can be expanded to provide beneficial aspects to the art of power distribution, containment of electromagnetic interference and electronic signal interconnect.

20 Encapsulated Circuit Assembly

FIGS. 1A-1C illustrate the construction of an encapsulated circuit assembly using the present invention. FIG. 1A illustrates an exploded view of assembly 100. Assembly 100 comprises first printed circuit board (PCB) 102, second PCB 104, and heat transfer device 106. First PCB 102 can be a single layer PCB or multi-layer PCB, where the
25 multi-layer PCB is comprised of alternating layers of conducting and non-conducting materials to allow electrical signals to be routed from device to device on the first PCB 102. Devices 108-116 are shown mounted on first PCB 102. Devices 114 and 116 are shown as being mounted on the opposite side of first PCB 102 as devices 108-112. This illustrates that first PCB 102 can have devices 108-116 mounted on both sides.

30 Device 108 is coupled to first PCB 102 via a Ball Grid Array (BGA) 118. BGA 118 provides electrical contacts between device 108 and first PCB 102. Other methods of electrical coupling between device 108 and first PCB 102 are possible, e.g., wire

bonding, solder connections, etc. Further, there can also be thermal coupling between device 108 and PCB 102 if desired.

Heat transfer device 106 couples device 108 to second PCB 104. Heat transfer device 106 is typically a thermally conductive material, e.g., thermal grease, thermal epoxy, or a commercially produced material such as THERMA-GAPTM. Heat transfer device 106 provides a thermal interface between device 108 and the second PCB 104. Heat transfer device 106 is typically a mechanically compliant material to allow for minimal applied pressure to the device 108 such that device 108 is not subjected to additional stress through use of heat transfer device 108.

10 Spacers 141 and fasteners 142 provide for a precision alignment between boards 102 and 104 and the device 108 such that a controlled gap exists in which heat transfer device 106 can properly be accommodated without deleterious air gaps nor excessive pressure applied to device 108. Additionally, the location of the spacers 141 adjacent to the device 108 reduce variations in spacing caused by bow and warpage of board 102
15 and, to some extent, board 104.

Devices 110-116 that are thermally active but do not require heat transfer device 106 to cool the devices 110-116 and are cooled by conduction through first PCB 102, or through convection should air flow be available across first PCB 102. Otherwise, additional devices 110-116 can be coupled to second PCB 104 through additional heat
20 transfer devices 106. The present invention is not limited to a single device 108 that is cooled through the use of heat transfer device 106. Any number of devices 108-116 can be cooled through the use of single or multiple heat transfer devices 106.

Second PCB 104 is mechanically coupled to first PCB 102 through the use of fasteners 120 and standoffs 122. Fasteners 120 are typically screws, but can be other
25 types of fasteners such as rivets, hollow feedthroughs, connectors, or other fasteners. Standoffs 122 are typically unthreaded inserts with a height equal to the height of spacer 141. The fasteners 120 and standoffs 122 are located at mechanically and/or electrically desirable locations on first PCB 102. These locations are typically at the periphery of first PCB 102, but can be anywhere on first PCB 102.

30 Second PCB 104 has areas 124 that are designed to facilitate the transfer of heat from device 108, through heat transfer device 106, to a heat sink. Areas 124 comprise plated through holes (PTHs) 126, consisting of holes in board 104 with interior walls of

plated copper or other high thermally conductive material. In addition, the region within the hole may be filled with metal, liquid filled areas, or other thermal transfer devices or mechanisms to enhance thermal conduction between the material 106 and the heatsink 130. Areas 124 can be designed to be the same size, a larger size, or a smaller size than the device 108, depending on the heat dissipation requirements for device 108 and the size of second PCB 104. An additional benefit of PTHs 126 is to provide a means of reducing air pockets in material 106 and to provide a volume where excesses of material 106 may flow in the case of a reduced gap between device 108 and board 104. Still another benefit of PTHs 126 can be to adjust the thermal conductivity of the paths of multiple devices 108 on a single first PCB 102 to the common "isothermal" heatsink 130 such that if the two devices 108 have differing heat flow then the conductivity in each thermal path can be adjusted such that the junction temperature of each device 108 will be the same. This can be beneficial in improving timing margins of digital devices.

15 Thermal interface 128 is coupled to second PCB 104 to equalize and transfer heat from device 108, through heat transfer device 106 and second PCB 104 area 124 to heat sink 130. Although shown as a finned heat sink, heatsink 130 can be any device, e.g., a heat pipe, or a layer on second PCB 104 that acts as an isothermal conduction layer to properly remove the heat generated by device 108. Thermal interface 128 can be
20 electrically conductive, or non-electrically conductive, depending on the design for second PCB 104. For example, if devices 108-116 need to be mounted on second PCB 104, thermal interface 128 should be electrically non-conductive so as not to interfere with signals travelling between devices 108-116 that are mounted on second PCB 104. Thermal interface 128 can be thermal epoxy or any other material which thermally and
25 mechanically bonds second PCB 104 to heatsink 130.

FIG. 1B illustrates the assembly 100 as a completed assembly. The thermal coupling of device 108, heat transfer device 106, second PCB 104 in conjunction with PTHs 126, thermal interface 128, and heatsink 130 provide a thermal path for heat generated by device 108 to be dissipated by heatsink 130. Further, airflow can be
30 provided to further cool device 108 and devices 110-116. Although shown as covering the entire area of second PCB 104, heatsink 130 can be larger or smaller than the area of second PCB 104. Heatsink 130 also acts as a mechanical stabilizer for assembly 100, to

provide additional mechanical stability for assemblies 100 that will experience more severe mechanical environments, e.g., vibration.

FIG. 1C illustrates assembly 100 in an isometric view. Heatsink 130 is shown as smaller than second PCB 104 and thermal interface 128 to illustrate the flexibility of the 5 design of the present invention. Airflow can again be provided to increase the heat dissipation capabilities of assembly 100.

Multiple Device Encapsulated Circuit Assembly

FIGS. 2A-2B illustrate the construction of an encapsulated circuit assembly using 10 the present invention for multiple heat generating integrated circuit devices. FIG. 2A illustrates an exploded view of assembly 100. Assembly 100 comprises first printed circuit board (PCB) 102, second PCB 104, and heat transfer device 106. First PCB 102 can be a single layer PCB or multi-layer PCB, where the multi-layer PCB is comprised of 15 alternating layers of conducting and non-conducting materials to allow electrical signals to be routed from device to device on the first PCB 102. Devices 108, 114-116, and 132 are shown mounted on first PCB 102. Devices 114 and 116 are shown as being mounted on the opposite side of first PCB 102 as devices 108 and 132. This illustrates that first PCB 102 can have devices 108, 114-116, and 132 mounted on both sides.

Devices 108 and 132 are coupled to first PCB 102 via a Ball Grid Array (BGA) 20 118. BGA 118 provides electrical contacts between devices 108 and 132 and first PCB 102. Other methods of electrical coupling between devices 108 and 132 and first PCB 102 are possible, e.g., Tape Automated Bonding (TAB), SMT, flip chip, etc. Further, there can also be thermal coupling between devices 108 and 132 and PCB 102 if desired.

Heat transfer device 106 couples device 108 to second PCB 104. Heat transfer 25 device 106 is typically a thermally conductive material, e.g., thermal grease, thermal epoxy, or a commercially produced material such as THERMA-GAP™. Heat transfer device 106 provides a thermal interface between device 108 and the second PCB 104. Heat transfer device 106 is typically a mechanically compliant material to allow for minimal applied pressure to the device 108 such that device 108 is not subjected to 30 additional stress through use of heat transfer device 108.

Spacers 141 and fasteners 142 provide for a precision alignment between boards 102 and 104 and the device 108 such that a controlled gap exists in which heat transfer

device 106 can properly be accommodated without deleterious air gaps nor excessive pressure applied to device 108. Additionally, the location of the spacers 141 adjacent to the device 108 reduce variations in spacing caused by bow and warpage of board 102 and, to some extent, board 104.

5 Devices 114-116 that are thermally active but do not require heat transfer device 106 to cool the devices 114-116 are cooled by conduction through first PCB 102, or through convection should air flow be available across first PCB 102.

10 Device 132 is another heat generating device similar to device 108. However, all devices 108 and 132 that will require additional cooling through heat transfer device 106, second PCB 104, and heatsink 130 are not the same size and/or height. Therefore, each device 108 and 132 must be treated individually using the present invention to best provide heat dissipation for each device 108 and 132. In FIG. 2A, device 132 is shown as having a height 134 smaller than height 136 of device 108. There can be many devices 15 108 and 132 of varying heights mounted on first PCB 102, all of which can be cooled by the assembly 100 of the present invention, through use of an additional thermal interface 138 and a thermally conductive spacer 140.

20 Thermal interface 138 provides a thermal path for device 132 that will allow heat generated by device 132 to be dissipated by heatsink 130. Thermal interface 138 can be similar to heat transfer device 106, but can also be a different thermal transfer material to provide a proper thermal dissipative path. As an example thermal interface 138 need not be mechanically compliant so long as thermal interface 106 above it is. Thus, the use 25 of a hardening thermal epoxy may be useful to hold spacer 140 in place during assembly.

25 Spacer 140 is provided to increase height 134 to approximate height 136. This allows device 108 and device 132 to contact heat transfer device 106, which in turn contacts second PCB 104 and heatsink 130 to transfer heat from devices 108 and 132 to heatsink 130. Spacer 140 is shown as larger in size than device 132, which can provide for heat spreading of the heat generated by device 132 to heatsink 130. Spacer 140 can be of any size relative to device 132. Further, there can be spacers 140 on more than one device 108 and 132.

30 Where height differences between devices are relatively small and power levels modest these height differences may beneficially be accommodated by selecting varying

thicknesses of heat transfer device 106 rather than utilizing thermal interface 138 and spacer 140.

Second PCB 104 is coupled mechanically to first PCB 102 through the use of fasteners 120 and standoffs 122. Fasteners 120 are typically screws, but can be other types of fasteners such as rivets, feedthroughs that are hollow, connectors, or other fasteners. Standoffs 122 are typically unthreaded inserts with a height equal to the height of spacer 141. The fasteners 120 and standoffs 122 are located at mechanically and/or electrically desirable locations on first PCB 102. These locations are typically at the periphery of first PCB 102, but can be anywhere on first PCB 102. Second PCB 104 has areas 124 that are designed to facilitate the transfer of heat from devices 108 and 132, through heat transfer device 106, to a heat sink. Areas 124 comprise plated through holes (PTHs) 126, consisting of holes in board 104 with interior walls of plated copper or other high thermally conductive material. In addition, the region within the hole may be filled with metal, liquid filled areas, or other thermal transfer devices or mechanisms to enhance thermal conduction between material 106 and heatsink 130. Areas 124 can be designed to be the same size, a larger size, or a smaller size than the device 108, depending on the heat dissipation requirements for device 108 and the size of second PCB 104. An additional benefit of PTHs 126 is to provide a means of reducing air pockets in material 106 and to provide a volume where excesses of material 106 may flow in the case of a reduced gap between device 108 and 104.

Thermal interface 128 is coupled to second PCB 104 to equalize and transfer heat from device 108, through heat transfer device 106 and second PCB 104 area 124 to heat sink 130. Although shown as a finned heat sink, heatsink 130 can be any device, e.g., a heat pipe, or a layer on second PCB 104 that acts as an isothermal conduction layer to properly remove the heat generated by device 108. Thermal interface 128 can be electrically conductive, or non-electrically conductive, depending on the design for second PCB 104. For example, if devices 108-116 need to be mounted on second PCB 104, thermal interface 128 can be electrically non-conductive so as not to interfere with signals travelling between devices 108-116 that are mounted on second PCB 104. Thermal interface 128 can be thermal epoxy or any other material which thermally and mechanically bonds board 104 to heatsink 130.

FIG. 2B illustrates the assembly 100 of FIG. 2A as a completed assembly. The thermal coupling of devices 108 and 132, heat transfer device 106, thermal interface 138, spacer 140, second PCB 104 in conjunction with PTHs 126, thermal interface 128, and heatsink 130 provide thermal paths for heat generated by devices 108 and 132 to be dissipated by heatsink 130. Further, airflow can be provided to further cool devices 108 and 132, as well as devices 110-116. Although shown as covering the entire area of second PCB 104, heatsink 130 can be larger or smaller than the area of second PCB 104. Heatsink 130 also acts as a mechanical stabilizer for assembly 100, to provide additional mechanical stability for assemblies 100 that will experience more severe mechanical environments, e.g., vibration.

FIG. 3A illustrates in plan and section views a molded plastic spacer 143 that may be used in place of spacers 141 around a device that must be thermally coupled to board 104. This spacer has clearance holes 145 for fasteners 142. Although spacer 143 is shown with four clearance holes 145, spacer 143 can have any number of clearance holes 145 without departing from the scope of the present invention. Imbedded metal spacers may be molded into holes 145 where it may be desirous to provide electrical contact between board 102 and board 104. Spacer 143 substantially surrounds device 108, but can take any shape desired. A feature of the spacer is pins 144 that engage in mating holes of board 102 and act to hold spacer 143 in place until final assembly of assembly 100. An additional benefit of spacer 143 is that it provides complete enclosure of device 108 to prevent accidental damage. Furthermore, spacer 143 may be used to provide thermal isolation between device 108 and the remainder of the board assembly 100.

FIG. 3B illustrates a molded plastic spacer 147 that may be used in place of spacers 141 which have been previously described as used to couple second PCB 104 to first PCB 102. This spacer 147 is shown as having ten clearance holes 150 for fasteners 120, however a larger or smaller number of fasteners may be used as the need and size of the PCBs 102 and 104 require. Imbedded metal spacers may be molded into holes 150 where it may be desirous to provide electrical contact between board 102 and board 104. Furthermore, the entire molded assembly may be formed as a cast metal structure or other metallic form which may be useful in the containment of electromagnetic radiation. A feature of the spacer 147 is pins 149 that engage in mating holes of board 102 and act

to hold in place spacer 147 until final assembly of 100. An additional benefit of spacer 147 is that it provides complete enclosure of device 108 to prevent accidental damage. Furthermore, spacer 147 may be used to provide environmental isolation to the internal components of assembly 100.

5

Embodiments Of The Present Invention

FIGS. 4A-4C illustrate the construction of a printed circuit board using the present invention. FIG. 4A illustrates an exploded view of assembly 100. Assembly 100 comprises first printed circuit board (PCB) 102, second PCB 104, and heat transfer device 106. First PCB 102 can be a single layer PCB or multi-layer PCB, where the multi-layer PCB is comprised of alternating layers of conducting and non-conducting materials to allow electrical signals to be routed from device to device on the first PCB 102. Devices 108, 114, and 116 are shown mounted on first PCB 102. Devices 114 and 116 are shown as being mounted on the opposite side of first PCB 102 as device 108. 10 This illustrates that first PCB 102 can have devices 108, 114, and 116 mounted on both sides. 15

Device 108 is coupled to first PCB 102 via a Ball Grid Array (BGA) 118. BGA 118 provides electrical contacts between device 108 and first PCB 102. Other methods of electrical coupling between device 108 and first PCB 102 are possible, e.g., wire bonding, solder connections, etc. Further, there can also be thermal coupling between 20 device 108 and PCB 102 if desired.

Heat transfer device 106 couples device 108 to second PCB 104. Heat transfer device 106 is typically a thermally conductive material, e.g., thermal grease, thermal epoxy, or a commercially produced material such as THERMA-GAP™. Heat transfer 25 device 106 provides a thermal interface between device 108 and the second PCB 104. Heat transfer device 106 is typically a mechanically compliant material to allow for minimal applied pressure to the device 108 such that device 108 is not subjected to additional stress through use of heat transfer device 108.

Spacers 141 and fasteners 142 provide for a precision alignment between boards 30 102 and 104 and the device 108 such that a controlled gap exists in which heat transfer device 106 can properly be accommodated without deleterious air gaps nor excessive pressure applied to device 108. Additionally, the location of the spacers 141 adjacent to

the device 108 reduce variations in spacing caused by bow and warpage of board 102 and, to some extent, board 104.

Devices 114-116 that are thermally active but do not require heat transfer device 106 to cool the devices 114-116 are cooled by conduction through first PCB 102, or 5 through convection should air flow be available across first PCB 102. Otherwise, additional devices 114-116 can be coupled to second PCB 104 through additional heat transfer devices 106. The present invention is not limited to a single device 108 that is cooled through the use of heat transfer device 106. Any number of devices 108 can be cooled through the use of single or multiple heat transfer devices 106.

10 Second PCB 104 is coupled mechanically to first PCB 102 through the use of fasteners 120 and standoffs 122. Fasteners 120 are typically screws, but can be other types of fasteners such as rivets, hollow feedthroughs, connectors, or other fasteners. Standoffs 122 are typically unthreaded inserts with a height equal to the height of spacer 141. The fasteners 120 and standoffs 122 are located at mechanically and/or electrically 15 desirable locations on first PCB 102. These locations are typically at the periphery of first PCB 102, but can be anywhere on first PCB 102.

Second PCB 104 has areas 124 that are designed to facilitate the transfer of heat from device 108, through heat transfer device 106, to a heat sink. Areas 124 comprise plated though holes (PTHs) 126, consisting of holes in board 104 with interior walls of 20 plated copper or other high thermally conductive material. In addition, the region within the hole may be filled with metal or other thermal transfer devices or mechanisms to enhance thermal conduction between the material 106 and the heatsink 130. Areas 124 can be designed to be the same size, a larger size, or a smaller size than the device 108, depending on the heat dissipation requirements for device 108 and the size of second 25 PCB 104. An additional benefit of PTHs 126 is to provide a means of reducing air pockets in material 106 and to provide a volume where excesses of material 106 may flow in the case of a reduced gap between device 108 and board 104. Still another benefit of PTHs 126 can be to adjust the thermal conductivity of the paths of devices 108 and 132 to the common "isothermal" lateral heat spreader block 146 such that if the 30 two devices have differing heat flow then the conductivity in each path can be adjusted such that the junction temperature of each device will be the same. This can be beneficial in improving timing margins of digital devices.

Thermal interface 128 is coupled to second PCB 104 to equalize and transfer heat from device 108, through heat transfer device 106 and second PCB 104 area 124 to lateral heat spreader block 146. Heat spreader block 146 is desirably of a thermally high conductivity material such as aluminum which allows the heat emanating from devices 5 108 and 132 to flow to heat sink 130 which is located outside of the volume used by boards 102 and 104. Additionally, heat spreader block 146 may incorporate imbedded heat pipes to enhance lateral thermal conduction and/or reduce height. Although shown as a finned heat sink, heatsink 130 can be any device, e.g., a heat pipe, that can conduct heat out of the heat spreader block 146. Thermal interface 128 can be electrically 10 conductive, or non-electrically conductive, depending on the design for second PCB 104. For example, if devices 108-116 need to be mounted on second PCB 104, thermal interface 128 should be electrically non-conductive so as not to interfere with signals travelling between devices 108-116 that are mounted on second PCB 104. Thermal interface 128 can be thermal epoxy or any other material which thermally and 15 mechanically bonds board 104 to heatsink 130 and between heatsink 130 and heat spreader block 146.

As opposed to FIG. 1A, heatsink 130 is now shown as being mounted outboard the volume occupied by PCB 102 and second PCB 104. This flexibility of the present invention to mount the heatsink 130 at multiple locations provides additional design 20 capabilities, i.e., the height of assembly 100 is now independent of the height of heatsink 130. Thus, heat dissipative capability is provided without additional volume requirements for assembly 100 other than the height of heat spreader block 146.

FIG. 4B illustrates the assembly 100 as a completed assembly. The thermal coupling of device 108, heat transfer device 106, second PCB 104, thermal interface 128, 25 heat spreader block 146 and heatsink 130 provide a thermal path for heat generated by device 108 to be dissipated by heatsink 130. Further, airflow can be provided to further cool device 108 and devices 114-116. Heatsink 130 can be larger or smaller than the height of PCB 102, PCB 104 and heat spreader block 146. Heat spreader block 146 also acts as a mechanical stabilizer for assembly 100, to provide additional mechanical stability 30 for assemblies 100 that will experience more severe mechanical environments, e.g. vibration.

FIG. 4C illustrates assembly 100 in an isometric view. Heatsink 130 is shown as residing outboard of first PCB 102 and second PCB 104. Thermal interface 128 is shown on the opposite side of second PCB 104, and is shown as smaller than second PCB 104 to illustrate the flexibility of the design of the present invention. Airflow can again be provided to increase the heat dissipation capabilities of assembly 100.

The design of FIGS. 4A-4C can be used where assembly 100 height is at a premium, or, where the heatsink 130 would be more efficient located outboard first PCB 102 and second PCB 104 than it would be if heatsink 130 sat atop second PCB 104. This might occur when it is desirous to locate assembly 100 adjacent to similar assemblies 100 as close as practical to minimize electrical interconnect lengths, where airflow over the top of second PCB 104 is less than airflow outboard of assembly 100. Further, the placement of heatsink 130 outboard first PCB 102 and second PCB 104 allows heatsink 130 to be electrically grounded, or placed at a desired potential, using both first PCB 102 and second PCB 104.

15

Thermal Considerations

FIGS. 5A and 5B illustrate the thermal considerations of a printed circuit board embodying the present invention.

FIG. 5A illustrates assembly 100 with the various thermal interfaces described for the present invention. The silicon die is represented as die 148. Thermal Interface 1 (TI1) 172 is the thermal interface internal to the device 108 between device heatspreader 178 and silicon die 148. Heatspreader 178 may not always be present in which case thermal interface 172 would be used to represent the thermal resistance of the outside package surface to the silicon die 148, e.g. molding compound. Thermal Interface 2 (TI2) 174 is the interface between second PCB 104 and device 108. Thermal Interface 3 (TI3) 176 is the interface between second PCB 104 and heatsink 130.

Plated through holes (PTH) 180 is the area 124 of PCB 104 that allows thermal conduction through the board 104. Heatsink (HSK) 130 is the device that couples the heat flow to the air or in some cases to thermal pipes to remote radiators. FIG. 5B illustrates the thermal schematic for the assembly 100 shown in FIG. 5A. Starting from die 148, TI1 172 receives a thermal resistance value, theta TI1 (θ_{TI1}) 186, HS1 178 receives a thermal resistance value theta HS1 (θ_{HS1}) 188, TI2 174 receives a thermal

resistance value, theta TI2 (θ_{TI2}) 190, HV 180 receives a thermal resistance value, theta HV (θ_{HV}) 192, TI3 176 receives a thermal resistance value, theta TI3 (θ_{TI3}) 194, and HSK 130 receives a thermal resistance value, theta HSK (θ_{HSK}) 202. The thermal resistances of the assembly 100 are determined in terms of degrees centigrade per watt (C/W). To

5 determine the total temperature rise across the interface from silicon die 148 to ambient air, the total power of the device is multiplied by the total thermal resistance:

$$\Delta T = \sum_{i=1}^n \theta_i * W$$

For example, a 1 C/W total thermal resistance for a 50 Watt device would yield a total temperature change of 50 C.

10

FIG. 6 illustrates a flow chart describing the steps used in practicing the present invention.

Block 204 represents the step of mounting a heat generating device on a first printed circuit board.

15

Block 206 represents the step of thermally coupling the heat generating device to a heatsink coupled to a second printed circuit board, wherein a thermal path passes through the second printed circuit board.

Conclusion

20

This concludes the description of the preferred embodiment of the invention. The following describes some alternative embodiments for accomplishing the present invention. Assembly 100 can have both rigid and flexible layers to accommodate the needs of PCB designers without departing from the scope of the present invention. Further, the thicknesses of assembly 100 can be modified to accommodate components 25 as needed.

25

Although described with respect to thermal considerations, the present invention can also be used to shield device 108 from outside radiative effects, e.g., radiation, electromagnetic interference, etc. Further, device 108 can be shielded from emitting radiation and/or electromagnetic signals to the outside world through the use of the 30 present invention. The present invention can also be used to provide power to devices

through the second PCB 104 by contacting the device 108 through spacers 124 or standoffs 122.

In summary, the present invention discloses an encapsulated circuit assembly and a method for making such an assembly. The assembly comprises a first printed circuit board, a second printed circuit board, and a heat transfer device. The second printed circuit board comprises a heatsink, and the heat transfer device couples between a device mounted on the first printed circuit board and the second printed circuit board for transferring heat from the device to the heatsink of the second printed circuit board.

The foregoing description of the preferred embodiment of the invention has been presented for the purposes of illustration and description. It is not intended to be exhaustive or to limit the invention to the precise form disclosed. Many modifications and variations are possible in light of the above teaching. It is intended that the scope of the invention be limited not by this detailed description, but rather by the claims appended hereto.

WHAT IS CLAIMED IS:

1. A printed circuit board assembly, comprising:
 - a first printed circuit board;
 - a second printed circuit board, coupled to the first printed circuit board, wherein
 - 5 the second printed circuit board comprises a heatsink; and
 - a heat transfer device, wherein the heat transfer device couples between a device mounted on the first printed circuit board and the second printed circuit board, for transferring heat from the device to the heatsink of the second printed circuit board.
- 10 2. The printed circuit board assembly of claim 1, wherein the second printed circuit board further comprises an area designed to transfer heat from the device to the heatsink.
- 15 3. The printed circuit board assembly of claim 1, further comprising a plate, coupled between the heat transfer device and the second printed circuit board, for providing a larger contact area to the heatsink.
- 20 4. The printed circuit board assembly of claim 1, wherein the heatsink is selected from a group comprising a finned heat sink, a heat pipe, and a metallic layer.
5. The printed circuit board assembly of claim 1, wherein the heat transfer device is selected from a group comprising thermal grease, thermal epoxy, and metal.
- 25 6. The printed circuit board assembly of claim 1, wherein the second circuit board includes areas where devices are electrically coupled to the second circuit board.
7. The printed circuit board assembly of claim 1, wherein the device is mounted to the first printed circuit board using a ball grid array.
- 30 8. The printed circuit board assembly of claim 1, wherein multiple devices are thermally coupled to the second printed circuit board.

9. The printed circuit board assembly of claim 1, wherein the heatsink is situated outboard both the first printed circuit board and the second printed circuit board using a lateral heat spreader block.

5 10. The printed circuit board assembly of claim 1, further comprising a spacer, wherein the spacer substantially surrounds the device.

10 11. The printed circuit board assembly of claim 10, wherein the spacer comprises metal spacers for providing electrical conduction between the first printed circuit board and the second printed circuit board.

12. The printed circuit board assembly of claim 1, wherein a spacer is mounted substantially at the periphery of the first printed circuit board.

15 13. The printed circuit board assembly of claim 1, wherein the second printed circuit board includes plated through holes.

14. The printed circuit board assembly of claim 13, wherein the plated through holes substantially equalize temperatures between a first device and a second device.

15. The printed circuit board assembly of claim 1, wherein the heat transfer device has a variable height to accommodate a first device having a first height and a second device having a second height.

25 16. A method for dissipating heat from a device, comprising the steps of: mounting a heat generating device on a first printed circuit board; and thermally coupling the heat generating device to a heatsink coupled to a second printed circuit board, wherein a thermal path passes through the second printed circuit board.

17. The method of claim 16, wherein the second printed circuit board further comprises an area designed to transfer heat from the device to the heatsink.

18. The method of claim 16, further comprising the step of providing a larger 5 contact area for the heat generating device to the heatsink to dissipate more heat from the heat generating device.

19. The method of claim 16, wherein the heatsink is selected from a group comprising a finned heat sink, a heat pipe, and a metallic layer.

10

20. The method of claim 16, wherein the thermal coupling between the heat generating device and the second printed circuit board is selected from a group comprising thermal grease, thermal epoxy, and metal.

15

21. The method of claim 16, wherein the second circuit board includes areas where a device is electrically coupled to the second circuit board.

22. The method of claim 16, wherein the heat generating device is mounted to the first printed circuit board using a ball grid array.

20

23. The method of claim 16, wherein multiple heat generating devices are thermally coupled to the second printed circuit board.

25

24. The method of claim 16, wherein the heatsink is situated outboard both the first printed circuit board and the second printed circuit board.

30

25. A printed circuit board assembly manufactured by the steps comprising: mounting a heat generating device on a first printed circuit board; and thermally coupling the heat generating devices to a heatsink coupled to a second printed circuit board; wherein a thermal path passes through the second printed circuit board.

-21-

AMENDED CLAIMS

[received by the International Bureau on 8 December 2000 (08.12.00);
original claims 1-18, 20 and 22-26 replaced by new claims 1-17, 19 and 21-37; remaining
claims unchanged (5 pages)]

1. A circuit board assembly, comprising:
 - a first circuit board;
 - a second circuit board, coupled to the first circuit board, wherein at least a portion of the second circuit board is thermally coupled to a heatsink and wherein the second circuit board further comprises an area designed to transfer heat from a device mounted on the first circuit board to the heatsink; and
 - a heat transfer device, thermally coupled between the device mounted on the first circuit board and the second circuit board, for transferring heat from the device to the heatsink.
2. The circuit board assembly of claim 1, further comprising a thermal interface, coupled between the heat transfer device and the second circuit board, for providing a larger contact area to the heatsink.
3. The circuit board assembly of claim 1, wherein the heatsink is selected from a group comprising a finned heat sink, a heat pipe, and a metallic layer.
4. The circuit board assembly of claim 1, wherein the heat transfer device is selected from a group comprising thermal grease, thermal epoxy, and metal.
5. The circuit board assembly of claim 1, wherein the second circuit board includes at least one device electrically coupled to the second circuit board.
6. The circuit board assembly of claim 1, wherein the device is mounted to the first circuit board using a ball grid array.
7. The circuit board assembly of claim 1, wherein multiple devices are thermally coupled to the second circuit board.

30

8. The circuit board assembly of claim 1, wherein the heatsink is situated outboard both the first circuit board and the second circuit board using a lateral heat spreader block.

5 9. The circuit board assembly of claim 1, further comprising a spacer, wherein the spacer substantially surrounds the device.

10 10. The circuit board assembly of claim 9, wherein the spacer comprises metal spacers for providing electrical conduction between the first circuit board and the second circuit board.

11. The circuit board assembly of claim 1, wherein a spacer is mounted between the first circuit board and the second circuit board.

15 12. The circuit board assembly of claim 1, wherein the area of the second circuit board includes at least one plated through hole.

13. The circuit board assembly of claim 1, wherein:
the area of the second circuit board comprises a first plated through hole
20 thermally coupled to a first device and the heat sink;
the area of the second circuit board comprises a second plated through hole
thermally coupled to a second device and the heat sink; and
the first plated through hole and the second plated through hole substantially
equalize temperatures between the first device and the second device.

25 14. The circuit board assembly of claim 1, wherein the heat transfer device has a variable height to accommodate a first device having a first height and a second device having a second height.

30 15. A method for dissipating heat from a device, comprising the steps of:
mounting a heat generating device on a first circuit board; and

thermally coupling the heat generating device to a heatsink coupled to a second circuit board, wherein a thermal path passes through the second circuit board.

16. The method of claim 15, wherein the second circuit board further
5 comprises an area designed to transfer heat from the device to the heatsink.

17. The method of claim 15, further comprising the step of providing a
device thermally coupled to the heat generating device and the second circuit board, the
device having a larger contact area with the second circuit board than with the heat
10 generating device.

18. The method of claim 15, wherein the heatsink is selected from a group
comprising a finned heat sink, a heat pipe, and a metallic layer.

15 19. The method of claim 15, wherein the heat generating device is thermally
coupled to the second circuit board via a thermally conductive material selected from a
group comprising thermal grease, thermal epoxy, and metal.

20. The method of claim 15, wherein the second circuit board includes areas
where a device is electrically coupled to the second circuit board.

21. The method of claim 15, wherein the heat generating device is mounted
to the first circuit board using a ball grid array.

25 22. The method of claim 15, wherein multiple heat generating devices are
thermally coupled to the second circuit board.

23. The method of claim 15, wherein the heatsink is situated outboard both
the first circuit board and the second circuit board.

30 24. A circuit board assembly manufactured by the steps comprising:
mounting a heat generating device on a first circuit board; and

thermally coupling the heat generating device to a heatsink coupled to a second circuit board; wherein a thermal path passes through the second circuit board.

25. The circuit board of claim 24, wherein multiple heat generating devices
5 are thermally coupled to the second circuit board.

26. The circuit board assembly of claim 2, wherein the thermal interface is a
plate.

10 27. The circuit board assembly of claim 1, wherein a spacer is mounted
between the first circuit board and the second circuit board and substantially at the
periphery of the first printed circuit board.

15 28. The circuit board assembly of claim 1, wherein:
the first circuit board comprises a first device and a second device;
the second circuit board comprises a first plated through hole thermally coupled
to the first device and a second plated through hole thermally coupled to the second
device; and
an area of the first plated through hole and an area of the second plated through
20 hole are selected to equalize temperatures between the first device and the second device.

29. A circuit board assembly, comprising:
a first circuit board;
a heat generating device, mounted on the first circuit board;
25 a second circuit board, having a heat transfer area;
a heat transfer device, thermally coupled between the heat generating device and
the heat transfer area of the second circuit board.

30. The circuit board assembly of claim 29, further comprising a heat sink,
30 thermally coupled to the heat transfer area of the second printed circuit board.

31. The circuit board assembly of claim 29, further comprising a thermal interface, coupled between the heat transfer device at a first contact area and the second circuit board at a second contact area, wherein the second contact area is larger than the first contact area.

5

32. The circuit board assembly of claim 30, wherein the heatsink is selected from a group comprising a finned heat sink, a heat pipe, and a metallic layer.

10 33. The circuit board of claim 29, wherein the heat transfer device is selected from the group comprising thermal grease, thermal epoxy, and metal.

34. The circuit board of claim 29, further comprising at least one device electrically coupled to the second circuit board.

15 35. The circuit board of claim 29, wherein the heat transfer area of the second circuit board comprises at least one plated through hole.

36. The circuit board of claim 29, wherein:
the heat transfer area of the second circuit board comprises a first plated through
20 hole thermally coupled to a first device and the heat sink; and
the heat transfer area of the second circuit board comprises a second plated through
hole thermally coupled to a second device and heat sink.

25 37. The circuit board of claim 36, wherein the first plated through hole and the second plated through hole substantially equalize temperatures between the first device and the second device.

AMENDED SHEET (ARTICLE 19)

1/12

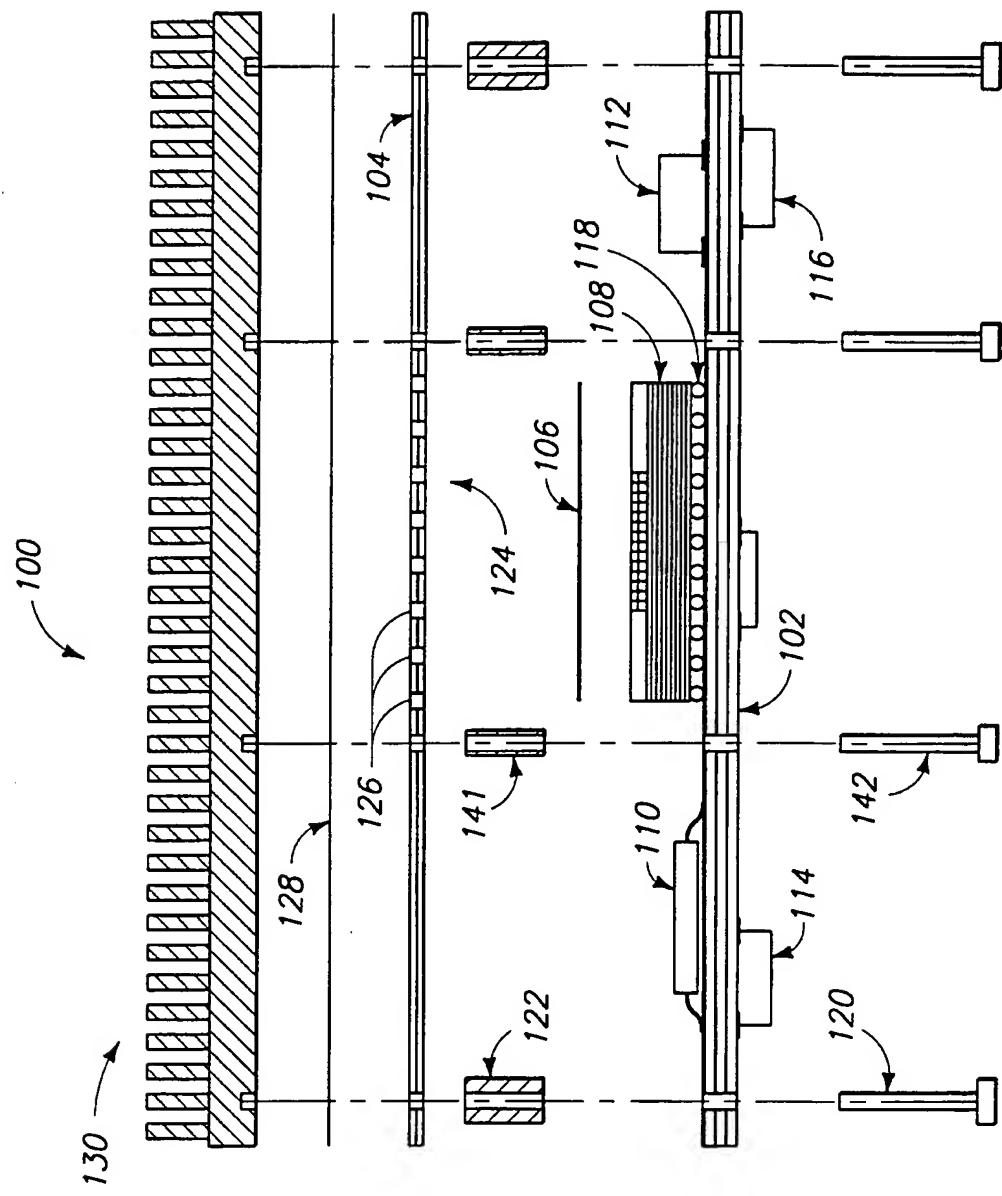


FIG. 1A

2/12

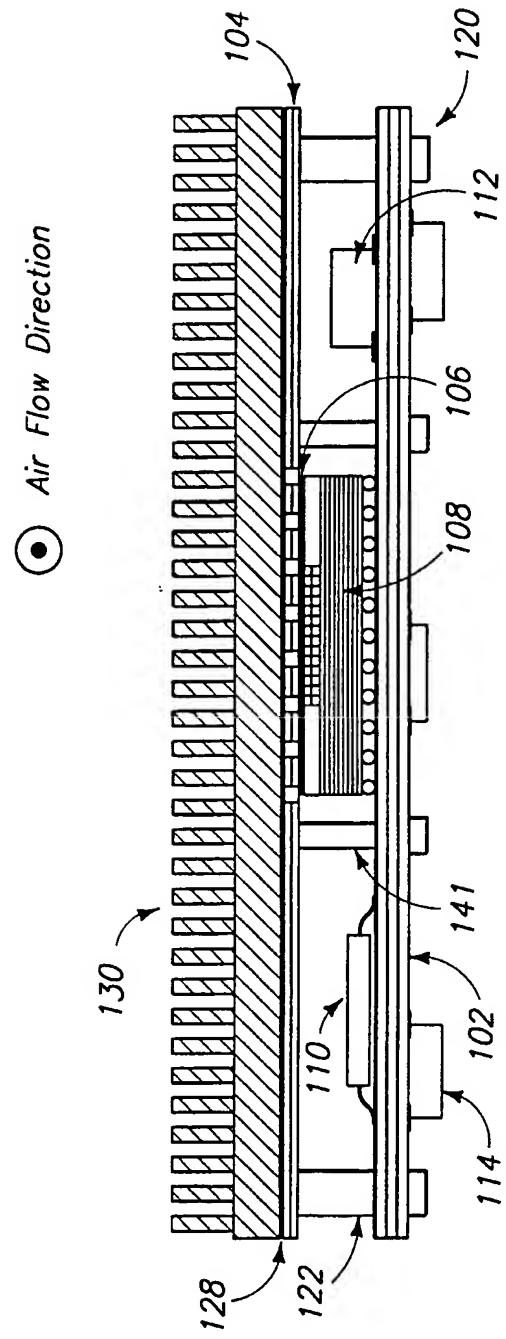


FIG. 1B

3/12

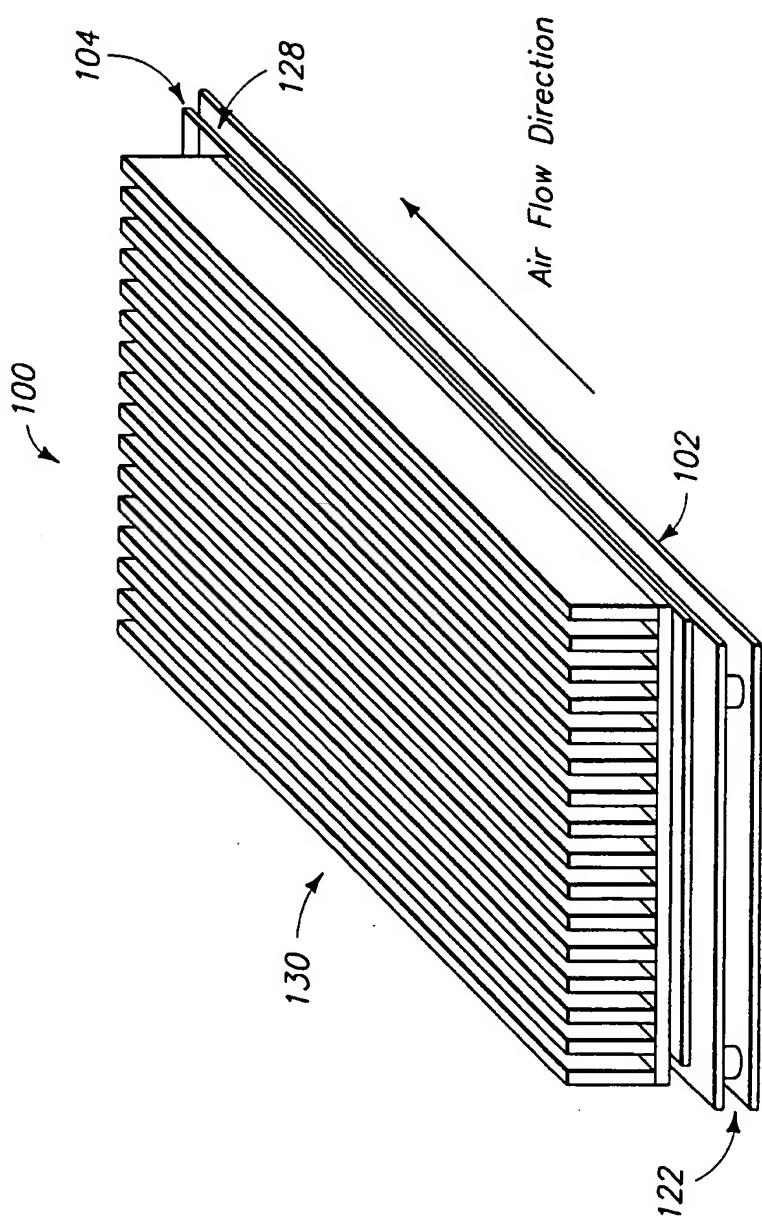


FIG. 1C

4/12

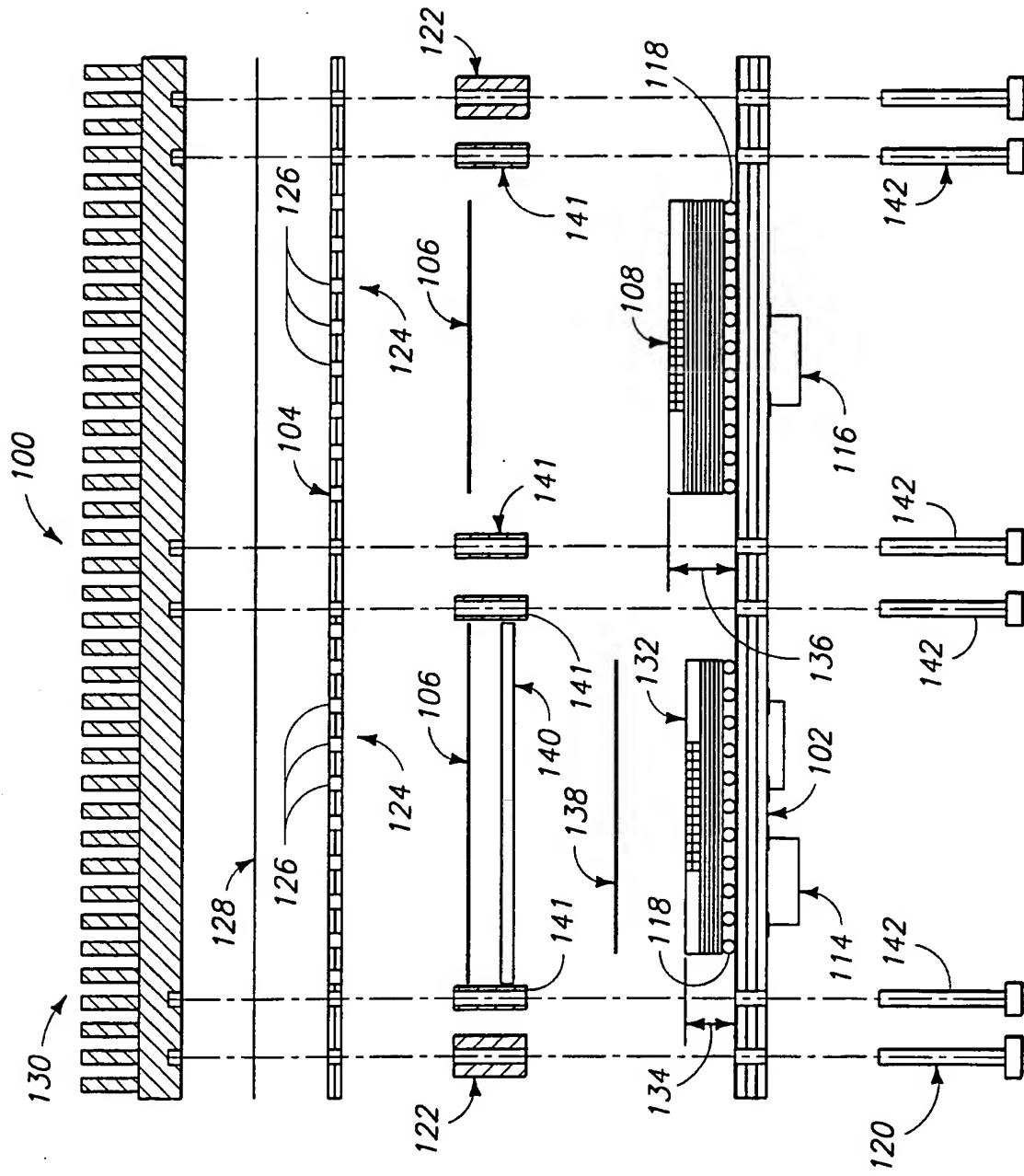


FIG. 2A

5/12

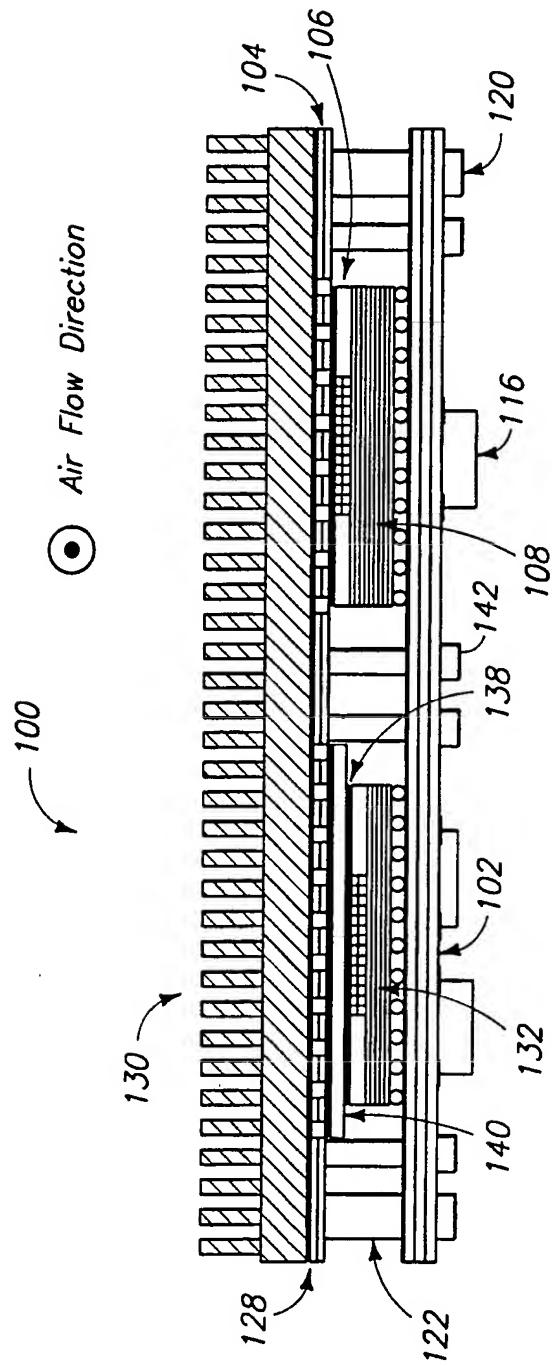
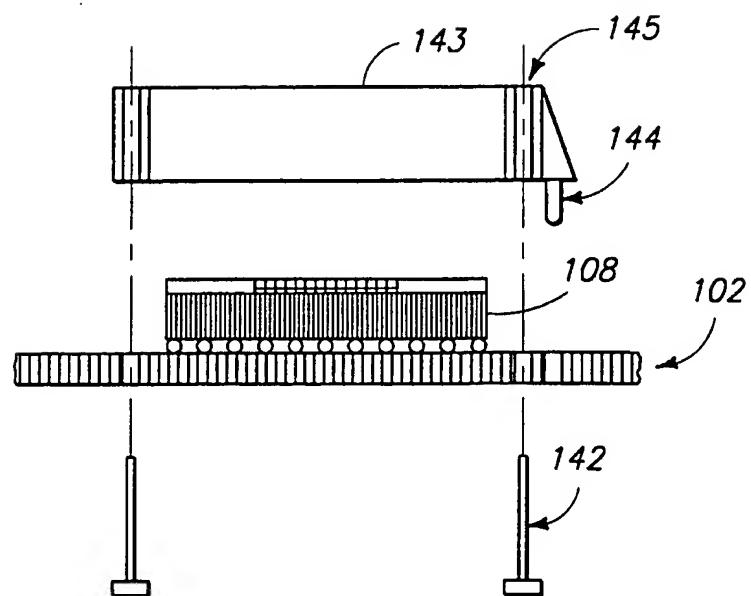
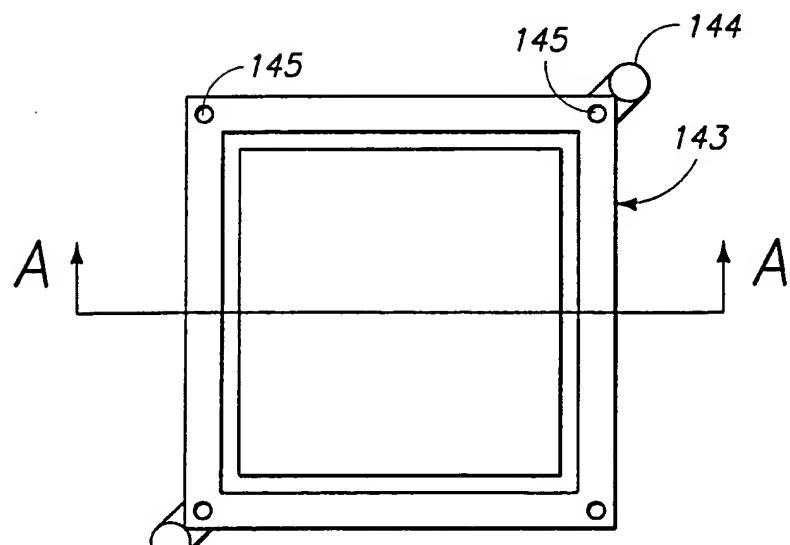


FIG. 2B

6/12



Section A-A

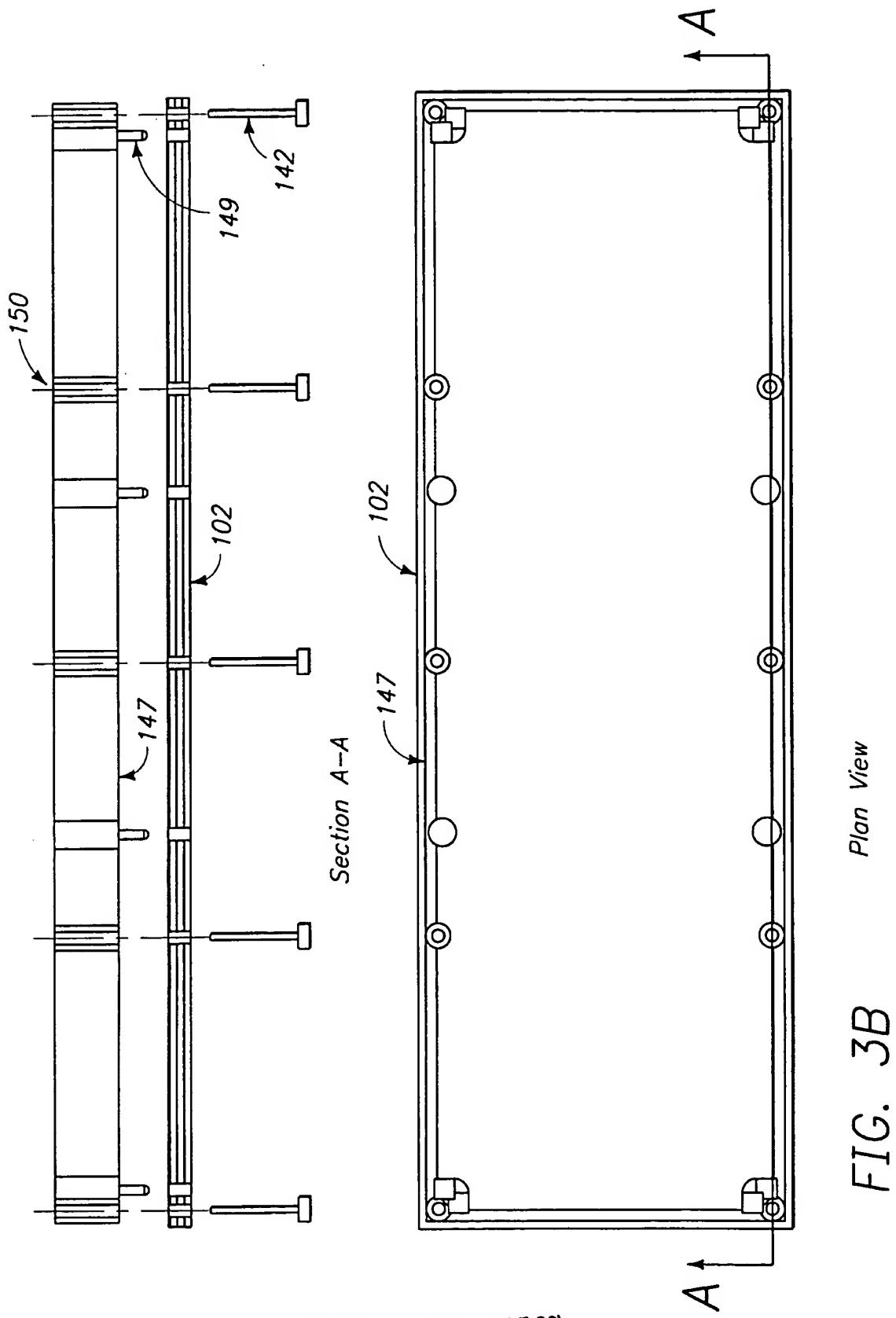


Plan View

FIG. 3A

SUBSTITUTE SHEET (RULE 26)

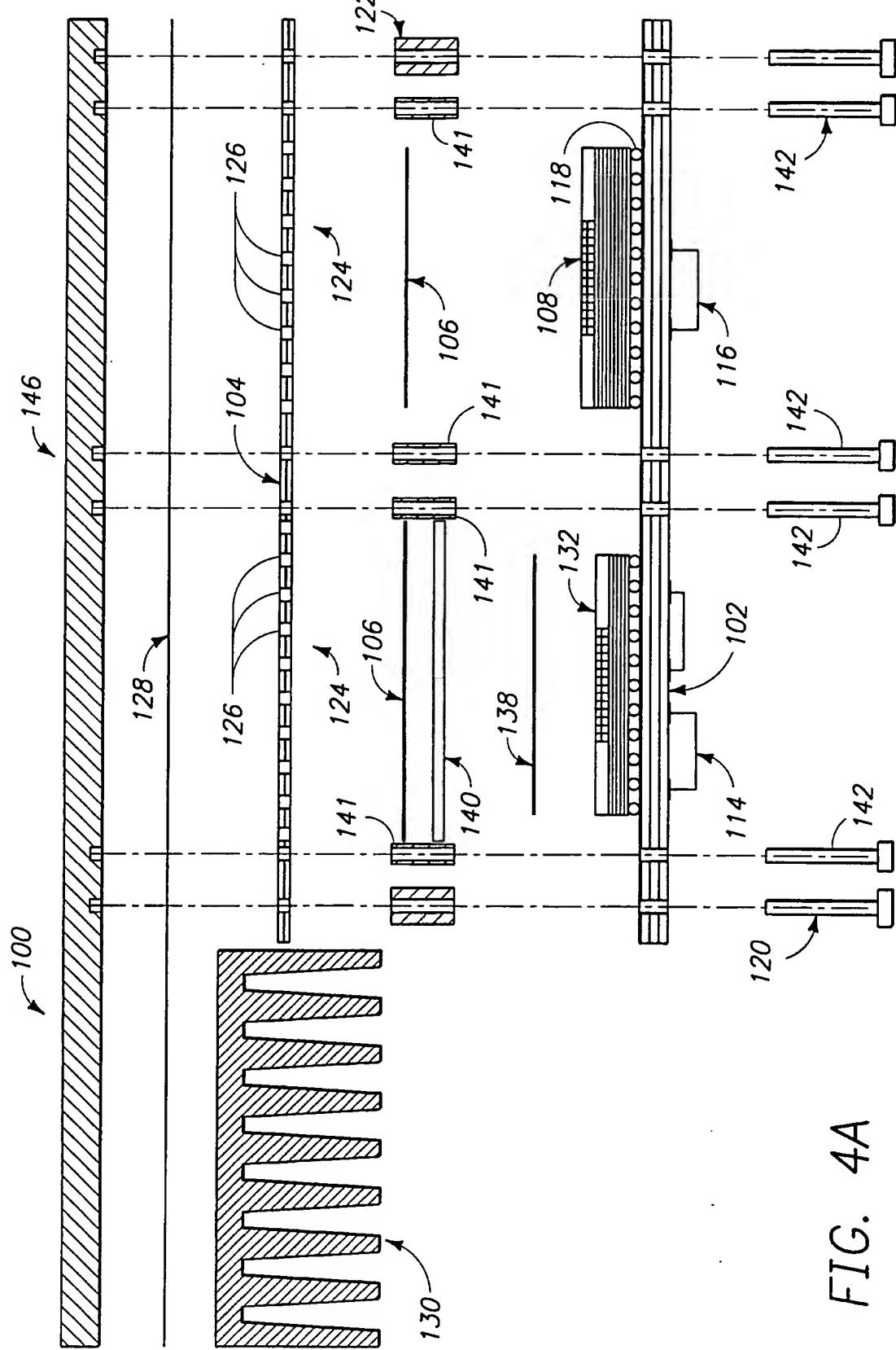
7/12



SUBSTITUTE SHEET (RULE 26)

FIG. 3B

8/12



SUBSTITUTE SHEET (RULE 26)

FIG. 4A

9/12

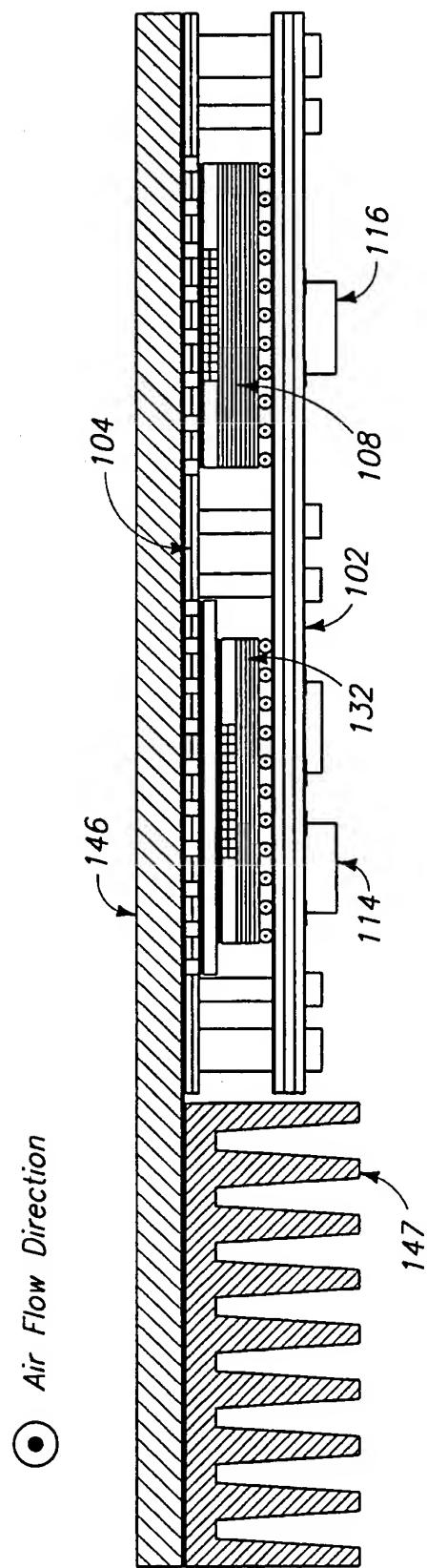


FIG. 4B

10/12

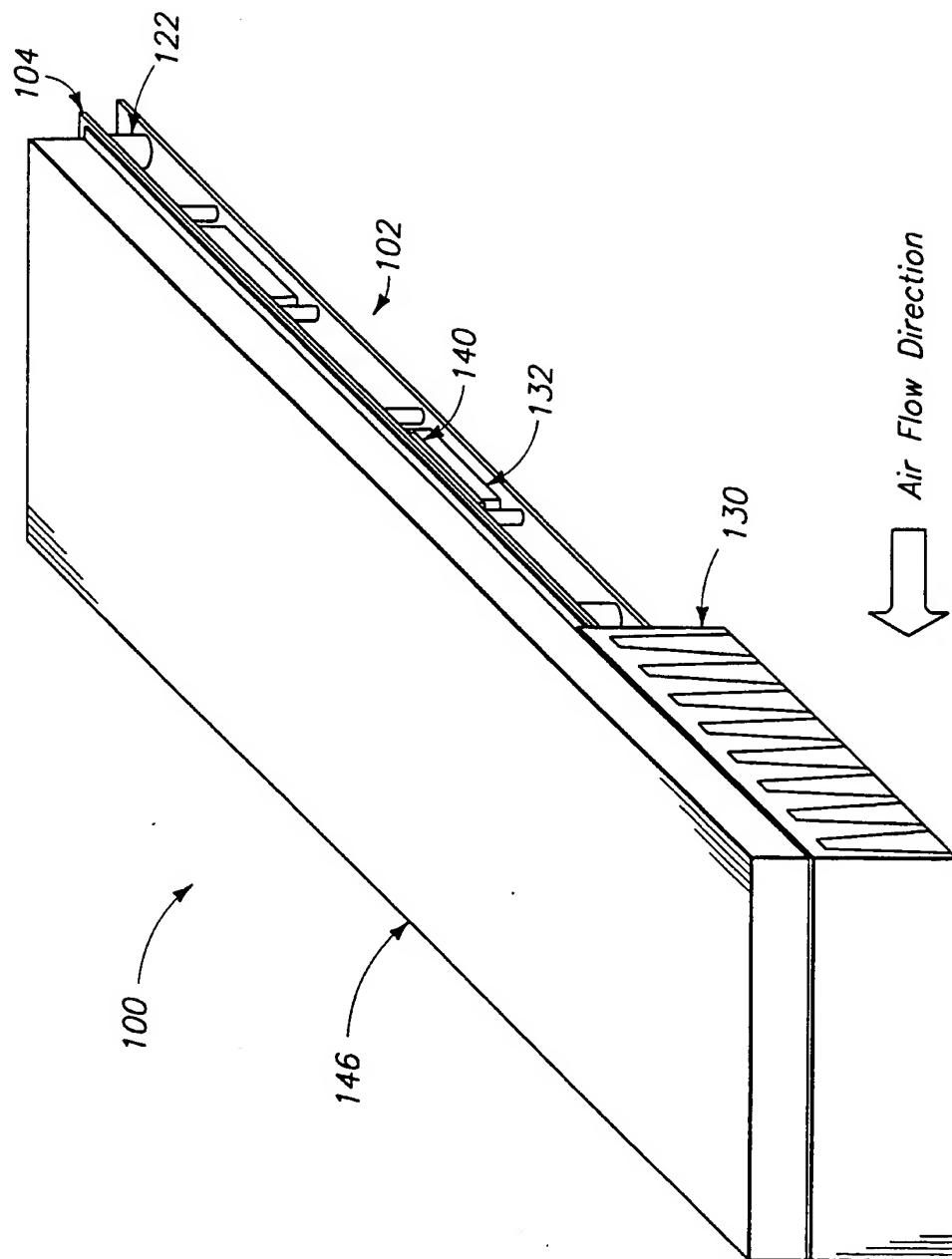


FIG. 4C

SUBSTITUTE SHEET (RULE 26)

11/12

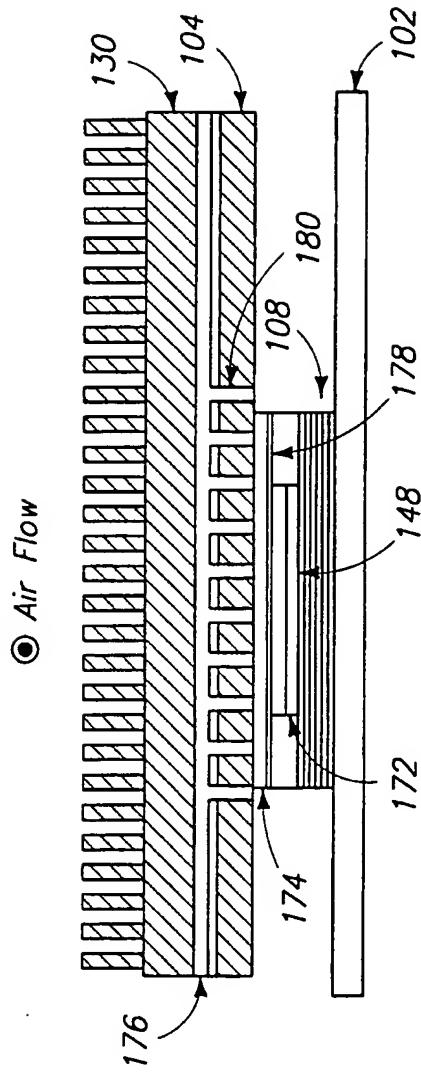


FIG. 5A

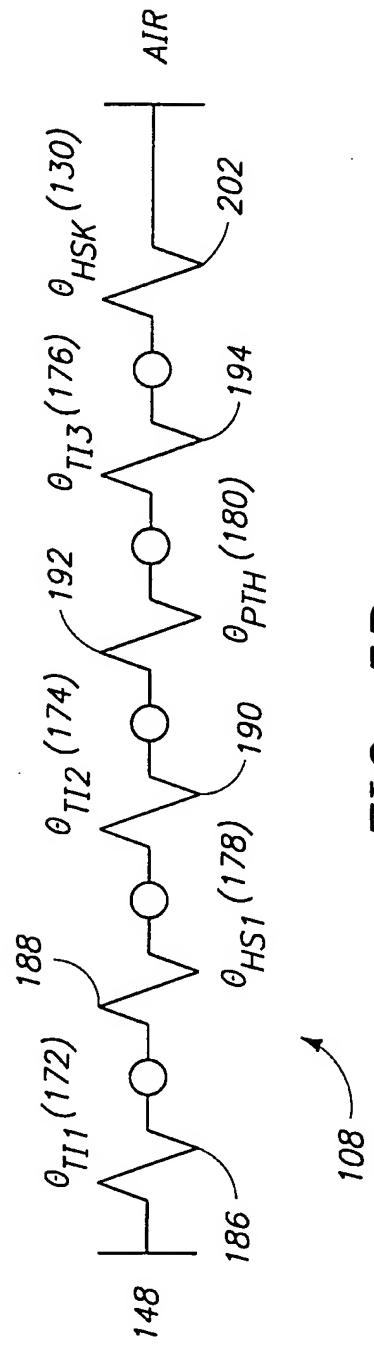


FIG. 5B

12/12

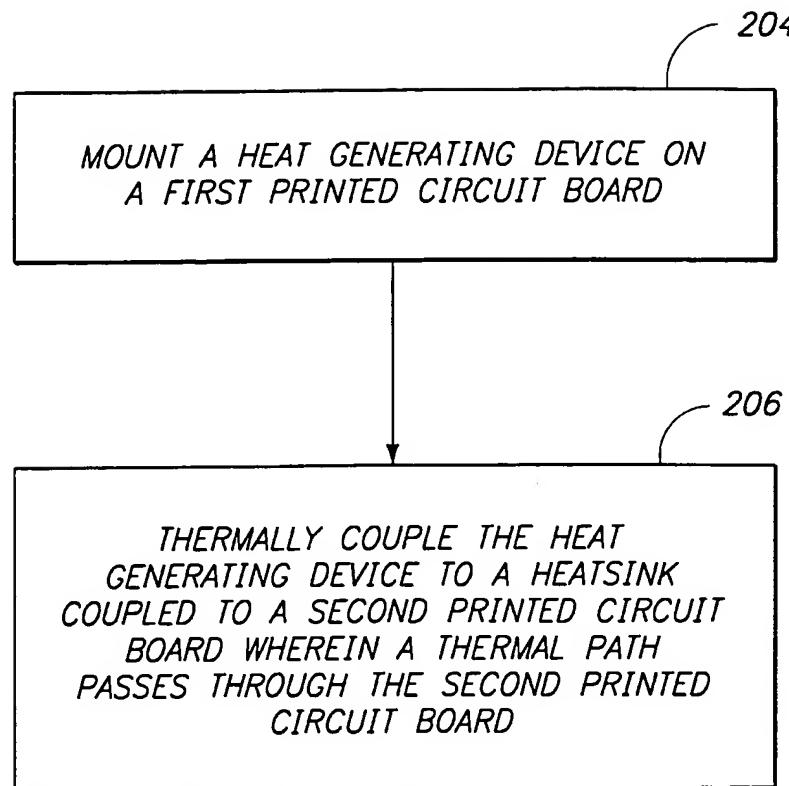


FIG. 6

INTERNATIONAL SEARCH REPORT

Inte	al Application No
PCT/US 00/18081	

A. CLASSIFICATION OF SUBJECT MATTER

IPC 7 H05K7/20

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

IPC 7 H05K

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

PAJ, EPO-Internal

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	JP 11 074427 A (SHARP CORP) 16 March 1999 (1999-03-16) figure 1	1-26
P, X	& US 5 995 370 A (NAKAMORI) 30 November 1999 (1999-11-30) the whole document ---	1-26
A	WO 96 23397 A (NORTHERN TELECOM LTD) 1 August 1996 (1996-08-01) ---	
A	US 5 708 566 A (HUNNINGHAUS ROY E ET AL) 13 January 1998 (1998-01-13) ---	
A	US 5 856 911 A (RILEY JOHN B) 5 January 1999 (1999-01-05) ---	
A	EP 0 717 443 A (MATSUSHITA ELECTRIC IND CO LTD) 19 June 1996 (1996-06-19) ---	
		-/-

Further documents are listed in the continuation of box C.

Patent family members are listed in annex.

* Special categories of cited documents :

- "A" document defining the general state of the art which is not considered to be of particular relevance
- "E" earlier document but published on or after the international filing date
- "L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)
- "O" document referring to an oral disclosure, use, exhibition or other means
- "P" document published prior to the international filing date but later than the priority date claimed

"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention

"X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone

"Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art.

"&" document member of the same patent family

Date of the actual completion of the international search	Date of mailing of the international search report
19 October 2000	26/10/2000
Name and mailing address of the ISA European Patent Office, P.B. 5818 Patentlaan 2 NL - 2280 HV Rijswijk Tel. (+31-70) 340-2040, Tx. 31 651 epo nl Fax: (+31-70) 340-3016	Authorized officer Castagné, 0

INTERNATIONAL SEARCH REPORTInt'l. Application No
PCT/US 00/18081**C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT**

Category	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	US 5 898 573 A (FUGARO ANTHONY SALVATORE) 27 April 1999 (1999-04-27) ---	
A	US 5 920 458 A (AZAR KAVEH) 6 July 1999 (1999-07-06) ---	
A	EP 0 920 055 A (BOSCH GMBH ROBERT) 2 June 1999 (1999-06-02) -----	

INTERNATIONAL SEARCH REPORT

Information on patent family members

Inte al Application No

PCT/US 00/18081

Patent document cited in search report		Publication date	Patent family member(s)		Publication date
JP 11074427	A	16-03-1999	US	5995370 A	30-11-1999
W0 9623397	A	01-08-1996	AU	697409 B	08-10-1998
			AU	4428396 A	14-08-1996
			CN	1169235 A	31-12-1997
			EP	0807372 A	19-11-1997
			JP	2908881 B	21-06-1999
			JP	10502773 T	10-03-1998
			CA	2166945 A	27-07-1996
			US	5646826 A	08-07-1997
US 5708566	A	13-01-1998	EP	0890297 A	13-01-1999
			JP	2000503486 T	21-03-2000
			WO	9819505 A	07-05-1998
US 5856911	A	05-01-1999	NONE		
EP 0717443	A	19-06-1996	DE	69508046 D	08-04-1999
			DE	69508046 T	15-07-1999
			KR	208053 B	15-07-1999
			CN	1130441 A, B	04-09-1996
			WO	9601498 A	18-01-1996
			JP	8078621 A	22-03-1996
US 5898573	A	27-04-1999	AU	9512998 A	23-04-1999
			WO	9917597 A	08-04-1999
US 5920458	A	06-07-1999	NONE		
EP 0920055	A	02-06-1999	DE	19752797 A	10-06-1999
			JP	11345921 A	14-12-1999